Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8445	((non-volatile adj2 memory) or (non-volatile adj2 storage) or EPROM or (flash adj2 memory)) same (memory near2 control\$3)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/03 15:36
L2	175	((non-volatile adj2 memory) or (non-volatile adj2 storage) or EPROM or (flash adj2 memory)) same (memory near2 control\$3) same (ECC or (error\$1 near2 correct\$4) or EDC or (error near2 correct\$4 near2 detect\$4))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/03 15:38
L3	78	((non-volatile adj2 memory) or (non-volatile adj2 storage) or EPROM or (flash adj2 memory)) same (memory near2 control\$3) same (ECC or (error\$1 near2 correct\$4) or EDC or (error near2 correct\$4 near2 detect\$4)) same (computer or host or CPU or processor or (external adj2 machine) or (external adj2 device))	USPAT; EPO; JPO; DERWENT	OR	OFF	-2005/03/03 15:40
L4	74	((non-volatile adj2 memory) or (non-volatile adj2 storage) or EPROM or (flash adj2 memory)) same (memory near2 control\$3) same (ECC or (error adj2 correct\$4) or EDC or (error near2 correct\$4 near2 detect\$4)) same (computer or host or CPU or processor or (external adj2 machine) or (extenal adj2 device))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/03 15:42
L11	123	((((non-volatile adj2 memory) or (non-volatile adj2 storage) or EPROM or (flash adj2 memory))) and ((memory near2 control\$3) same (ECC or (error adj2 correct\$4) or EDC or (error near2 correct\$4 near2 detect\$4))) and (computer or host or CPU or processor or (external adj2 machine) or (external adj2 device))) and (714/;,ccls.)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/03 15:47

L12	67	((((non-volatile adj2 memory) or (non-volatile adj2 storage) or EPROM or (flash adj2 memory))) and ((memory near2 control\$3) same (ECC or (error adj2 correct\$4) or EDC or (error near2 correct\$4 near2 detect\$4))) same (computer or host or CPU or processor or (external adj2 machine) or (external adj2 device))) and (714/;.ccls.)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/03 15:48
13	12	((((non-volatile adj2 memory) or (non-volatile adj2 storage) or EPROM or (flash adj2 memory))) same ((memory near2 control\$3) same (ECC or (error adj2 correct\$4) or EDC or (error near2 correct\$4 near2 detect\$4))) same (computer or host or CPU or processor or (external adj2 machine) or (external adj2 device))) and (714/;;ccls.)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/03:15:48

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O- By Author O- Basic O- Advanced O- CrossRef	1 The dependence of the output Stokes parameters on the state of an arbitrarily located polarization controller in PMD mitigation schemes Zadok, A.; Simon, N.; Eyal, A.; Lightwave Technology, Journal of , Volume: 22 , Issue: 6 , June 2004 Pages:1533 - 1538
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